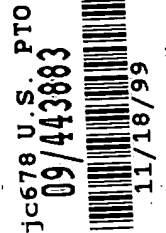


Inventor: Jerome Bombal; Laurent Souef

Title: "Method of Discriminating Between Different Types of Scan Failures, Computer Readable Code to Cause a Display to Graphically Depict One or More Simulated Scan Output Data Sets Versus Time and a Computer Implemented Circuit Simulation and Fault Detection System"

Assignee: VLSI Technology, Inc.

Attorney's Docket No.: VL5-062



INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: 28 Oct 1999

Inventor:
Jerome Bombal

Dated: 28 Oct 99

Inventor:
Laurent Souef

Dated: Nov 17, 1999

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